



Sheet 1 of 1

INFORMATION DISCLOSURE
CITATION IN AN
APPLICATION
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U.S. PATENT
APPLICATION NO.
10/511,720

APPLICANT
Kanji OTSUKA et al.

FILING DATE
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GROUP

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

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W	Isamu Kajitani, Tsutomu Hoshino, Masahiro Murakawa and Tetsuya Higuchi, "Implementation of the Circuit for Determining the Neural Network Structure with GAs," Japanese Neural Network Society Journal, Vol. 5, No. 4 (December 5, 1998), pp. 145-153
W	Kenichi Itoh, "Science and Technology," Nikkan Kogyo Shimibun, Ltd., July 28, 1998, pp. 74-88
W	Eikichi Yamashita, Hideyuki Ohashi and Kazuhiko Atsuki, "Simple CAD Formulas of Edge-Compensated Microstrip Lines," 1989 IEEE MTT-S International Microwave Symposium Digest (13-15 June 1989), pp. 339-342
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.